

Set Name **Query**
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result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L16</u>	L11 and data and address	4	<u>L16</u>
<u>L15</u>	L11 and data near3 bus	0	<u>L15</u>
<u>L14</u>	L11 and data near bus	0	<u>L14</u>
<u>L13</u>	L11 and data adj bus	0	<u>L13</u>
<u>L12</u>	L11 and data adj bus and address	0	<u>L12</u>
<u>L11</u>	L10 and operation adj circuit	4	<u>L11</u>
<u>L10</u>	L8 and (control\$3 adj display\$3) near3 data	110	<u>L10</u>
<u>L9</u>	L8 and control adj display\$3	669	<u>L9</u>
<u>L8</u>	memory near3 (stor\$3 adj information)	12213	<u>L8</u>

*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES;
OP=ADJ*

<u>L7</u>	L6 and memor\$3 near3 (control\$3 adj display)	5	<u>L7</u>
<u>L6</u>	display adj data adj line	260	<u>L6</u>
<u>L5</u>	L4 and sustain\$3 adj voltage	1	<u>L5</u>
<u>L4</u>	improv\$3 near3 (palc adj panel)	3	<u>L4</u>

DB=USPT; PLUR=YES; OP=ADJ

<u>L3</u>	L2 and sustain\$3	1	<u>L3</u>
<u>L2</u>	5948228 and voltage	3	<u>L2</u>
<u>L1</u>	5948228 and ionizable adj gas	3	<u>L1</u>

END OF SEARCH HISTORY

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 4 of 4 returned.**☐ 1. Document ID: US 5594908 A

L16: Entry 1 of 4

File: USPT

Jan 14, 1997

US-PAT-NO: 5594908

DOCUMENT-IDENTIFIER: US 5594908 A

TITLE: Computer system having a serial keyboard, a serial display, and a dynamic memory with memory refresh

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image										

☐ 2. Document ID: US 5218675 A

L16: Entry 2 of 4

File: USPT

Jun 8, 1993

US-PAT-NO: 5218675

DOCUMENT-IDENTIFIER: US 5218675 A

TITLE: Information display system suitable for compact electronic appliances having different display sizes

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
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☐ 3. Document ID: US 4942516 A

L16: Entry 3 of 4

File: USPT

Jul 17, 1990

US-PAT-NO: 4942516

DOCUMENT-IDENTIFIER: US 4942516 A

TITLE: Single chip integrated circuit computer architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image										

☐ 4. Document ID: US 4896260 A

L16: Entry 4 of 4

File: USPT

Jan 23, 1990

US-PAT-NO: 4896260

DOCUMENT-IDENTIFIER: US 4896260 A

TITLE: Data processor having integrated circuit memory refresh

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC
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L11 and data and address	4

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